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7590 02/04/2004

Paul J. Farrell, Esq.  
DILWORTH & BARRESE, LLP  
333 Earle Ovington Blvd.  
Uniondale, NY 11553

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|                        |                    |
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| <u>Paul J. Farrell</u> | (Depositor's name) |
| <u>Paul J. Farrell</u> | (Signature)        |
| <u>April 29, 2004</u>  | (Date)             |

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO.     | CONFIRMATION NO. |
|-----------------|-------------|----------------------|-------------------------|------------------|
| 10/042,604      | 01/09/2002  | Louis L. Hsu         | YOR9-2001-0578(728-225) | 6736             |

TITLE OF INVENTION: SEMICONDUCTOR MEMORY SYSTEM HAVING DYNAMICALLY DELAYED TIMING FOR HIGH-SPEED DATA TRANSFERS

| APPLN. TYPE    | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE   |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO           | \$1330    | \$300           | \$1630           | 05/04/2004 |

| EXAMINER            | ART UNIT | CLASS-SUBCLASS |
|---------------------|----------|----------------|
| AUDUONG, GENE NGHIA | 2818     | 365-233000     |

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Louis J. Percello and  
Dilworth & Barrese, LLP

3. \_\_\_\_\_

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INTERNATIONAL BUSINESS MACHINES CORPORATION

ARMONK, NEW YORK

Please check the appropriate assignee category or categories (will not be printed on the patent):  individual  corporation or other private group entity  government

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Issue Fee

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Paul J. Farrell Reg. No. 33,494 4/29/2004

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05/05/2004 GWDR00F2 00000052 500510 10042604

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02 FC:1504 300.00 DA

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